

Sheet 1 of 1

Form PTO-1449

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

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INFORMATION DISCLOSURE STATEMENT
BY APPLICANT

(Use several sheets if necessary)

APPLICANT

T. KAWAHARA ET AL.

FILING DATE

GROUP

August 20, 2001

2811

U.S. PATENT DOCUMENTS

| Examiner Initial | Document Number | Date | Name | Class | Subclass | Filing Date |
|------------------|-----------------|------|------|-------|----------|-------------|
| | AA | | | | | |
| | AB | | | | | |
| | AC | | | | | |
| | AD | | | | | |
| | AE | | | | | |
| | AF | | | | | |
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| | AL | | | | | |

FOREIGN PATENT DOCUMENTS

| Examiner Initial | Document Number | Date | Country | Class | Subclass | Translation/Abstract | |
|------------------|-----------------|------|---------|-------|----------|----------------------|----|
| | | | | | | Yes | No |
| | AM | | | | | | |
| | AN | | | | | | |
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| | AQ | | | | | | |
| | AR | | | | | | |
| | AS | | | | | | |
| | AT | | | | | | |

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

| | | | | | | | |
|-----|----|---|--|--|--|--|--|
| TWC | AU | IEEE Micro, "Design Challenges of Technology Scaling", Vol. 19, No. 4, pp. 23-29, 1999 | | | | | |
| TWC | AV | IEEE SPECTRUM, "Identifying Defects in Deep-Submicron CMOS ICS", pp. 66-71, September, 1996 | | | | | |
| TWC | AW | Symposium on VLSI Circuits Digest of Technical Papers, "Subthreshold-Current Reduction Circuits for Multi-Gigabit DRAMS", pp. 45-46, May 1993 | | | | | |
| TWC | AX | ISSCC Digest of Technical Papers, "50% Active-Power Saving without Speed Degradation Using Standby Power Reduction (SPR) Circuit", pp. 318-319, 1995. | | | | | |
| TWC | AY | Extended Abstract of the 1999 International Conference on Solid State Devices and Materials, "Suppression of Stand-by Tunnel Current in Ultra-Thin Gate Oxide MOSFETS", pp. 264-265, 1999 | | | | | |

Examiner

Date Considered 11/26/04



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